

Figure 1

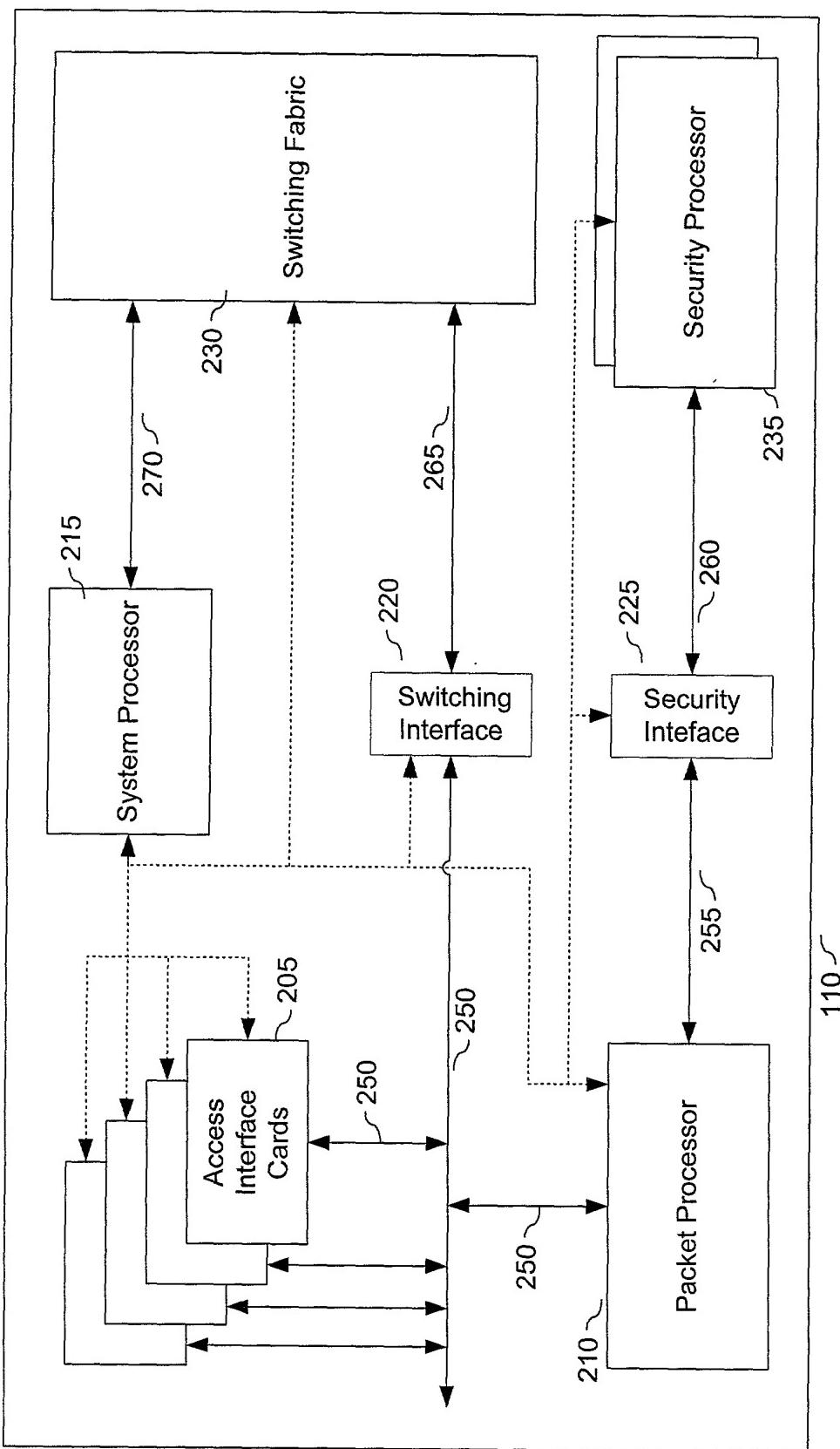


Figure 2

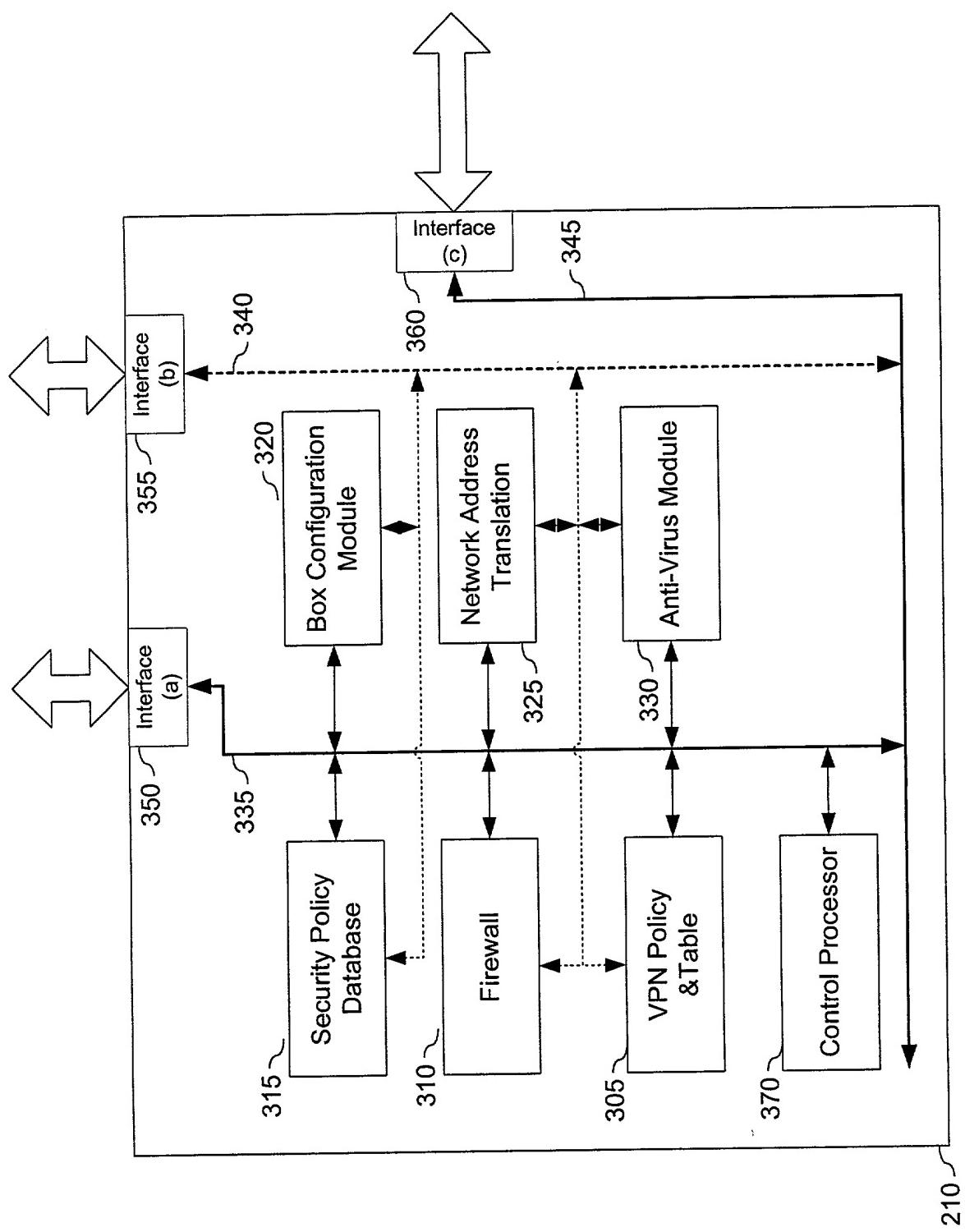


Figure 3
Packet Processor

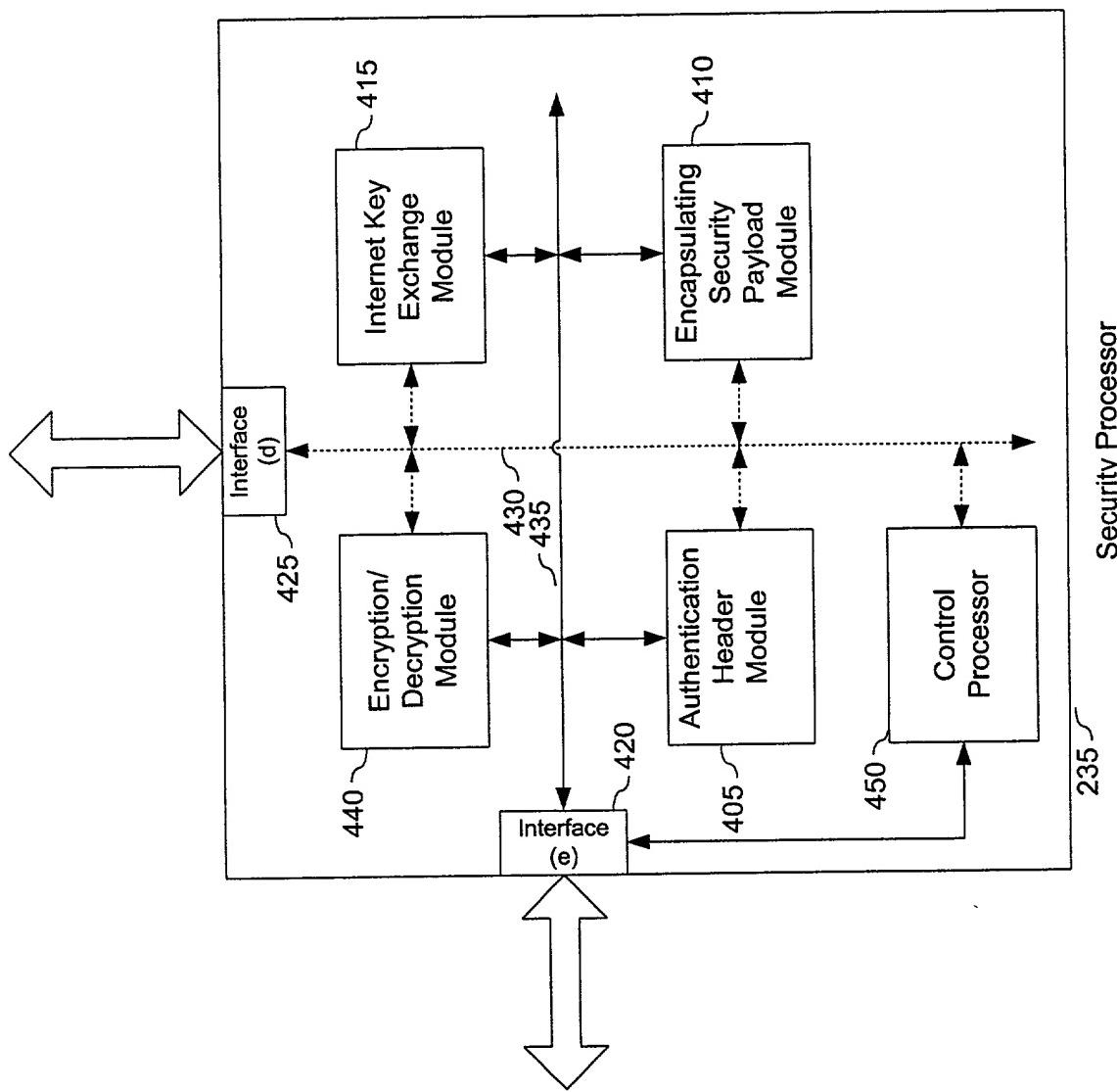
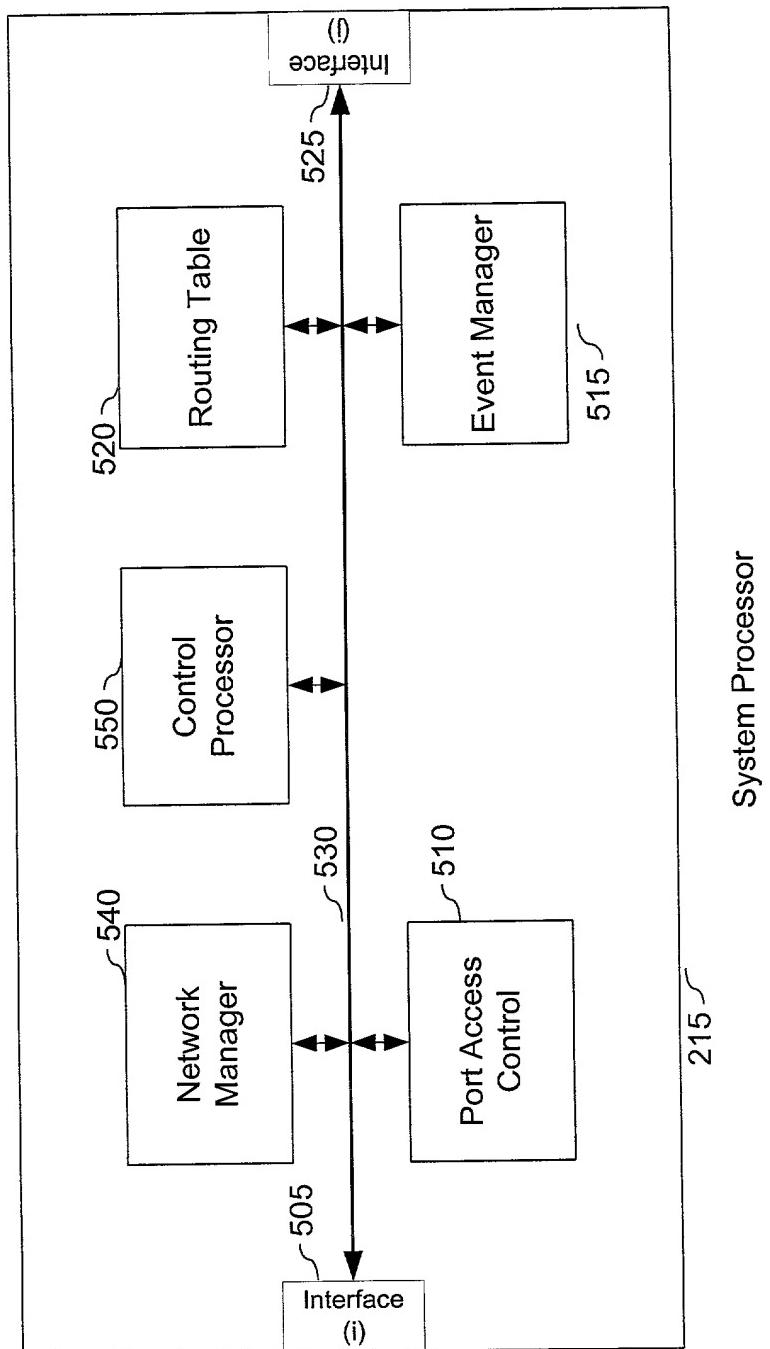


Figure 4
Security Processor



System Processor

Figure 5

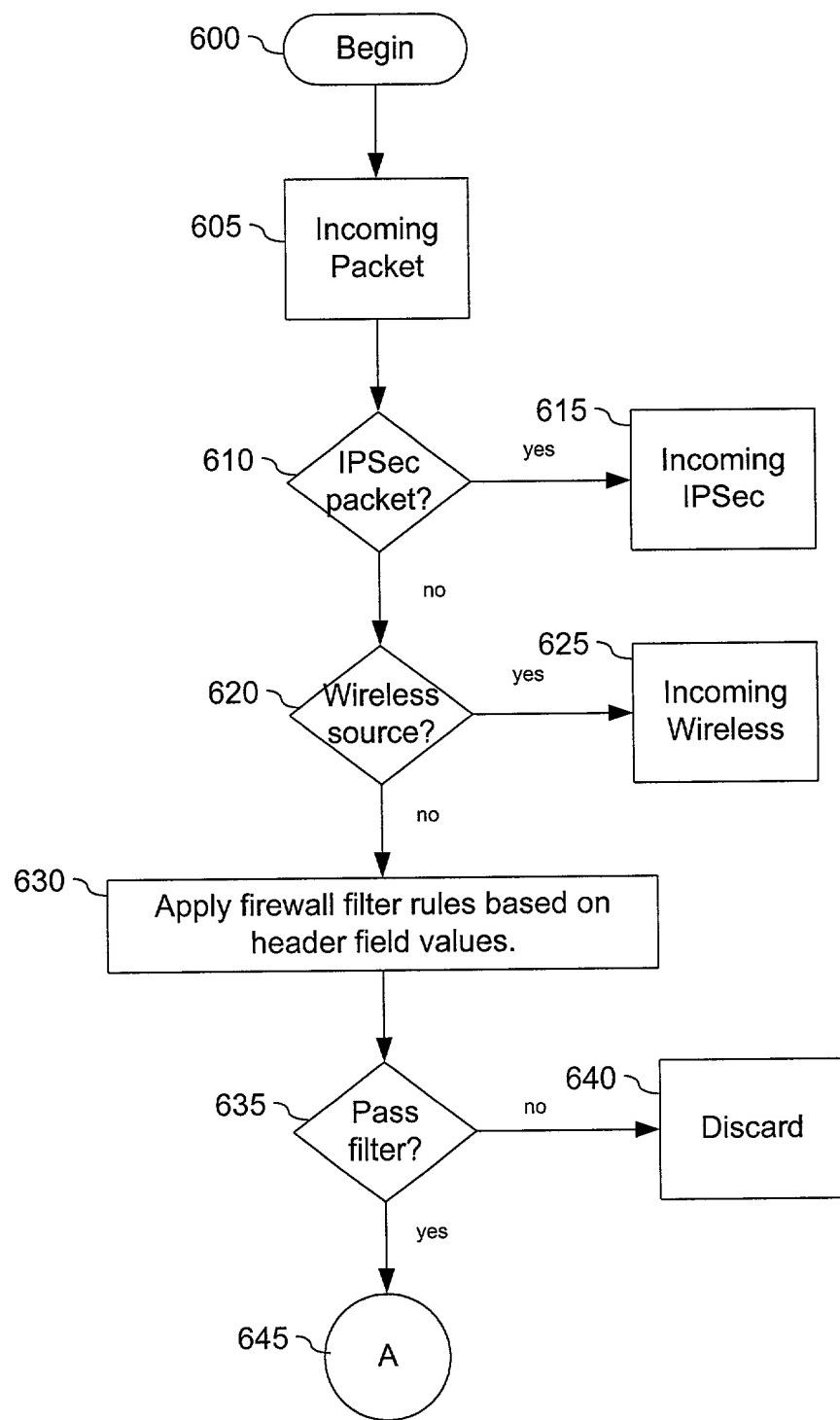


Figure 6A

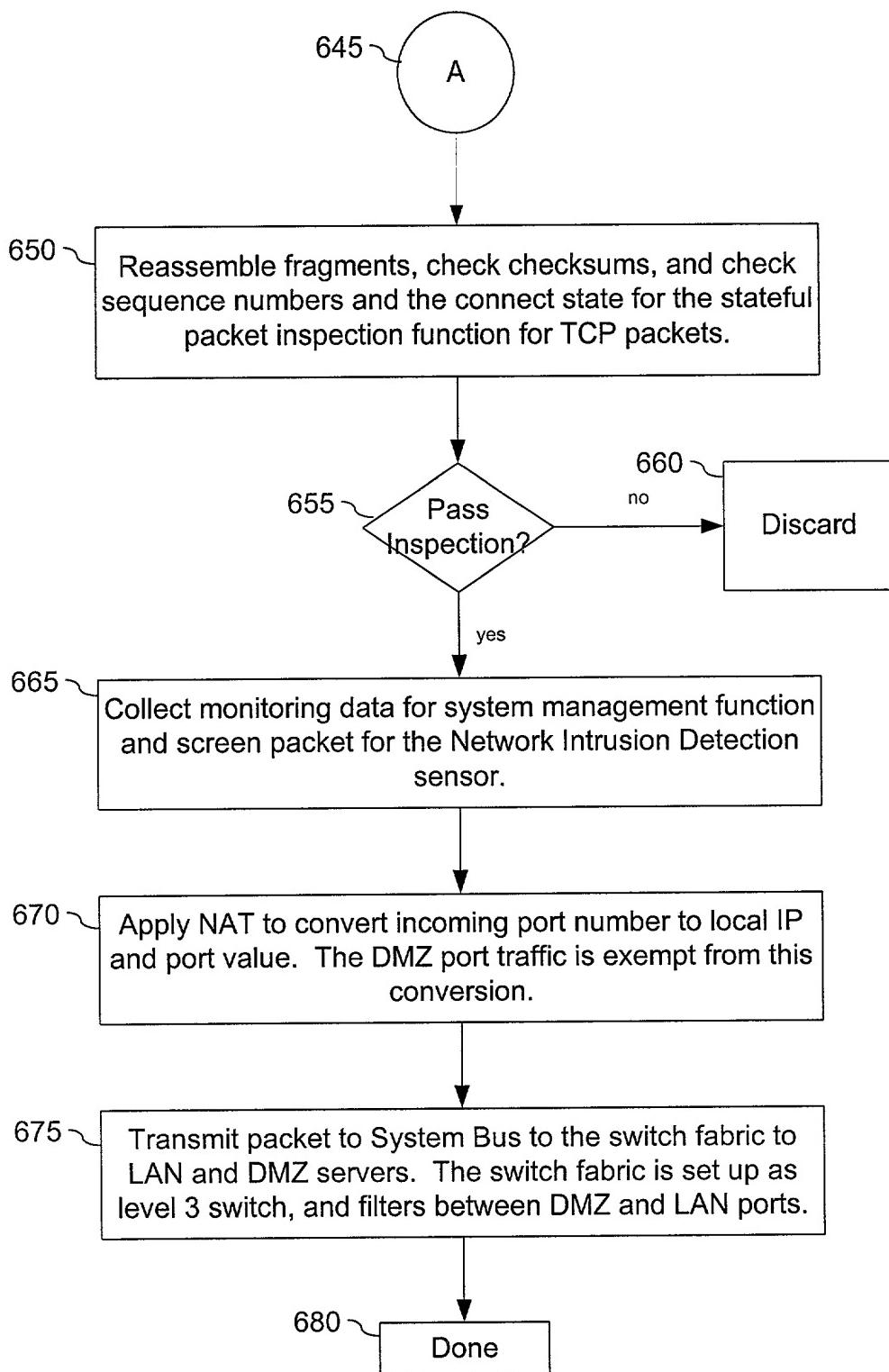


Figure 6B

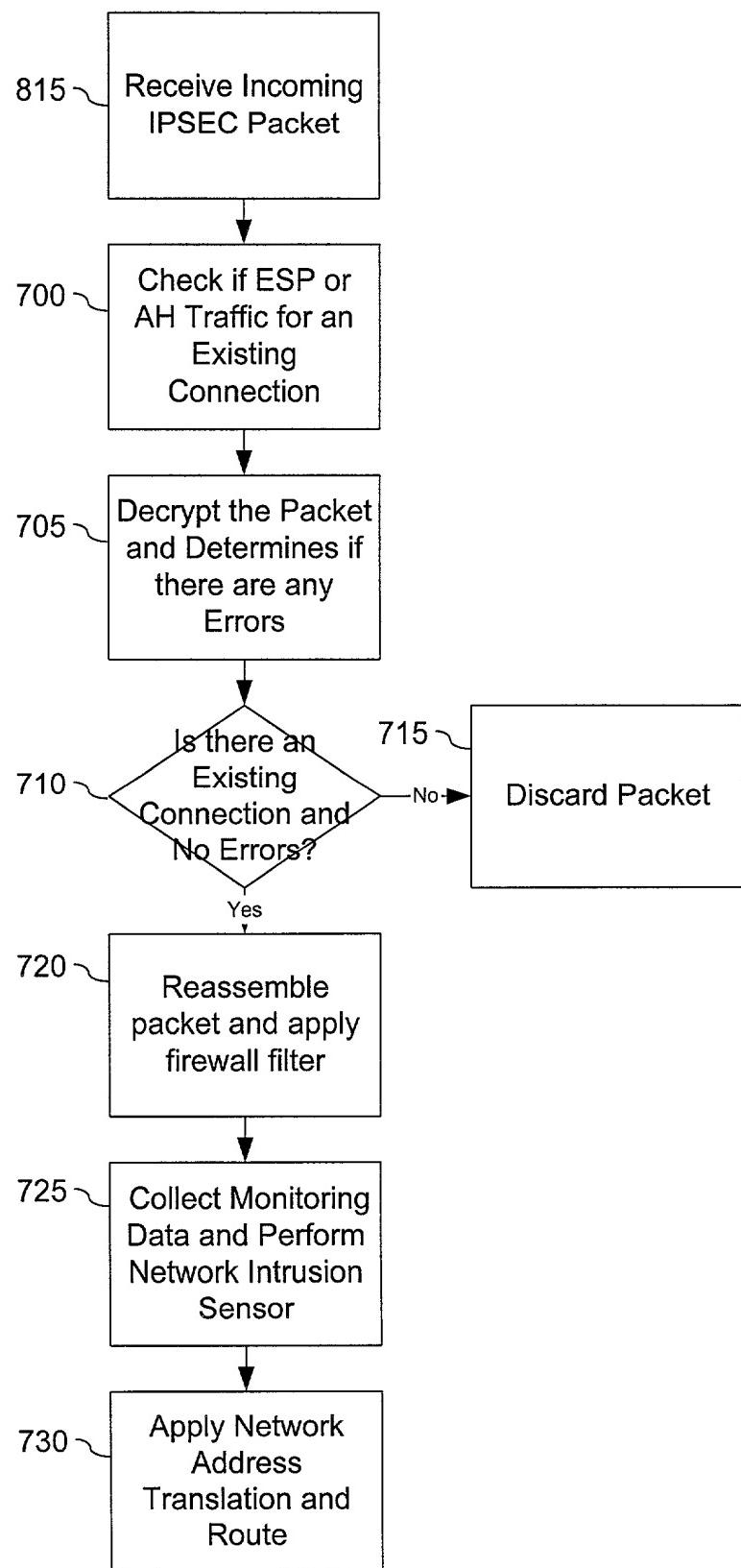


Figure 7

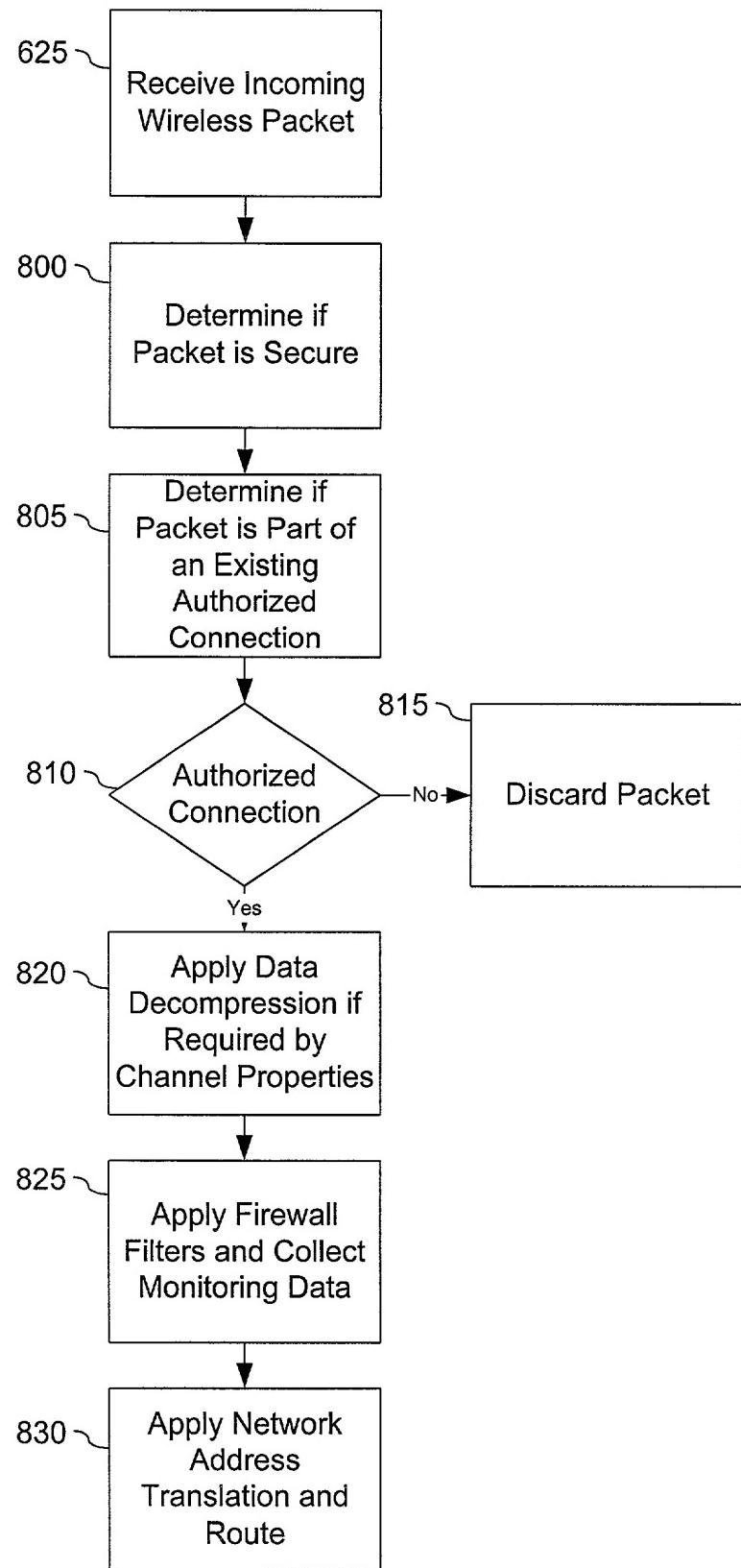


Figure 8

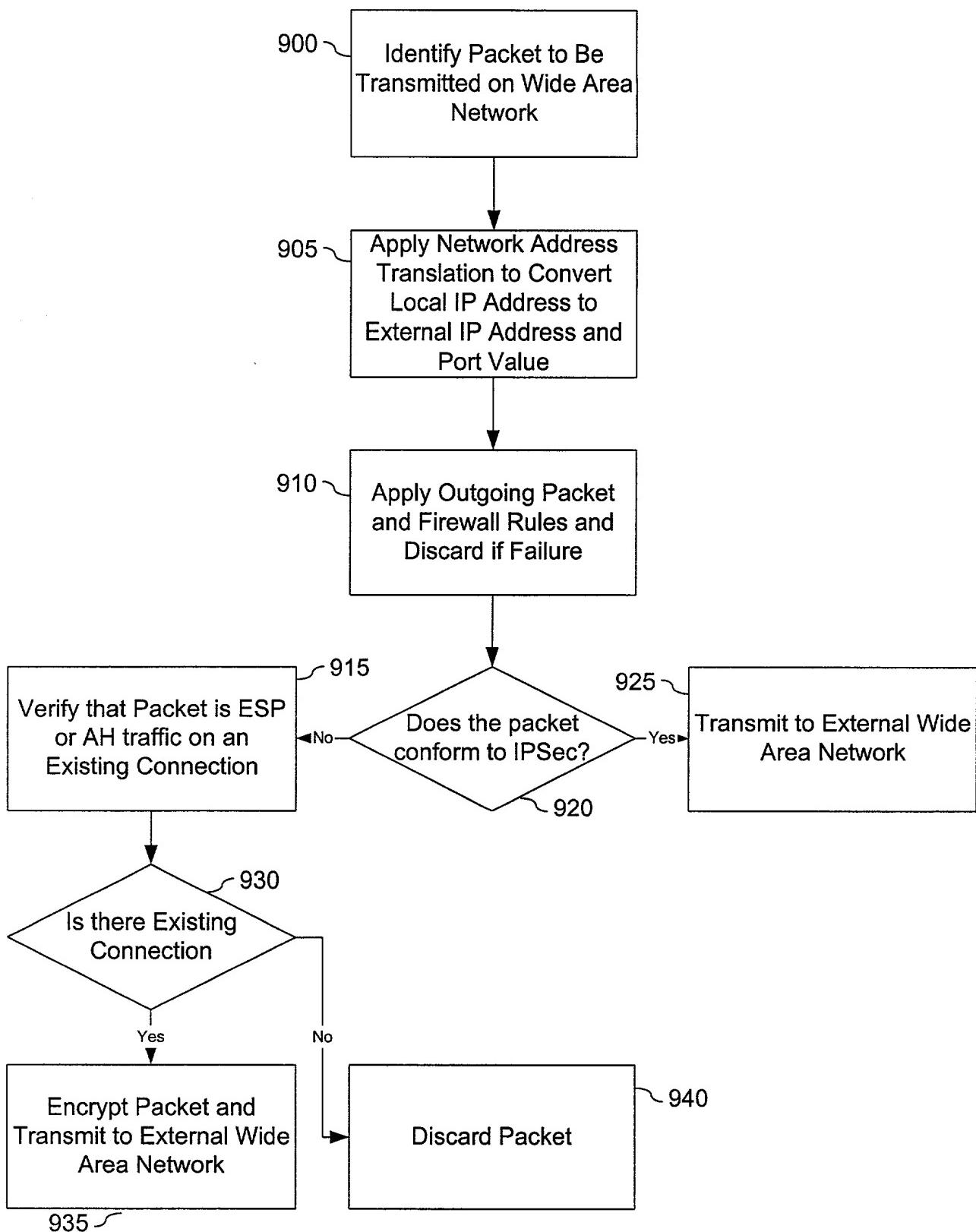


Figure 9

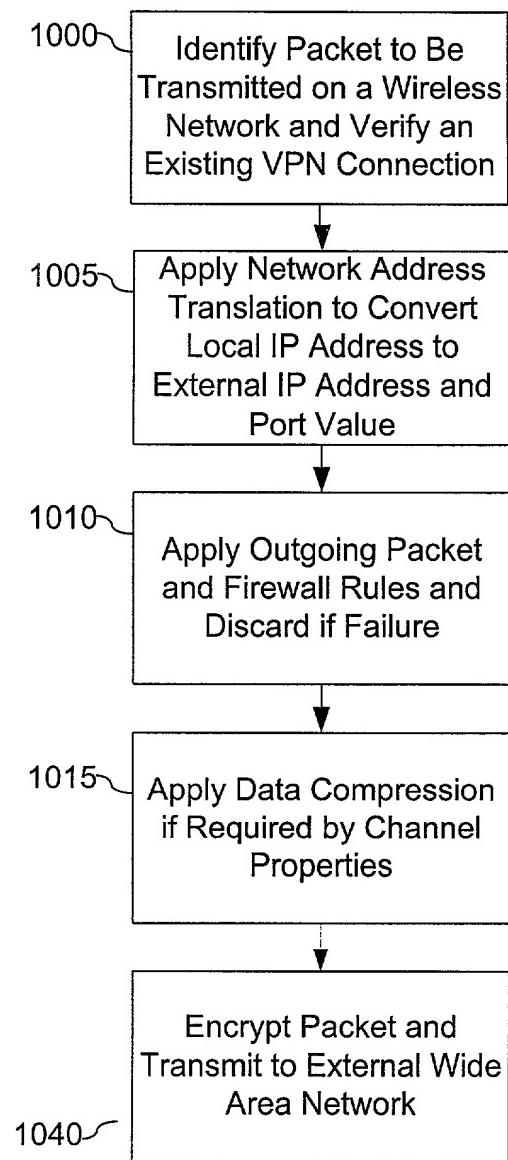


Figure 10